

New Simple Procedure for the Computation of the Multimode Admittance or Impedance Matrix of Planar Waveguide Junctions

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Abstract—Much effort has been devoted in the past to the development of electromagnetic simulation algorithms for waveguide junctions that could at the same time include higher-order mode interactions and lead to computationally efficient codes. Even though this is indeed a classical subject, there is still a strong interest in this area as waveguide systems become increasingly complex, and ever shorter development and manufacturing times are required. In this paper a simple method is described for the evaluation of the multimode network representation of planar waveguide junctions in terms of admittance or impedance parameters. The key feature of the method is that it starts from the wanted final results, the equivalent network representation, in order to obtain an analytic expression for the evaluation of the relevant matrix elements. The procedure is based on general network theory and is equivalent to ideally *measuring directly* the value of the admittance or impedance elements. In this paper the evaluation procedure is fully described. Measured results of actual hardware are then compared with simulations indicating that the codes developed are indeed very accurate as well as computationally very efficient.

I. INTRODUCTION

THE DESCRIPTION of the microwave properties of arbitrary planar waveguide junctions, like the one shown in Fig. 1, is a subject that has been studied considerably in the past. Of particular interest, from the engineering point of view, have been equivalent network representations, such as the ones that can be found in [1], for instance. The results in [1], however, only account for single mode interactions and are therefore not suitable for the design of modern microwave components. Higher order mode interactions have been traditionally accounted for using mode-matching procedures [2]. The resulting codes, however, although accurate, can be computationally very inefficient.

Recently, a multimode equivalent network representation for arbitrary waveguide junctions has been developed which is based on an integral equation formulation [3]. In [3], the imposition of the boundary conditions leads to an integral equation and to an equivalent network representation of the junction. Solving the integral equation allows for the subsequent computation of the multimode coupling matrix elements. Although this approach can be made computationally efficient, it requires substantial analytical work so that a better approach was deemed necessary.

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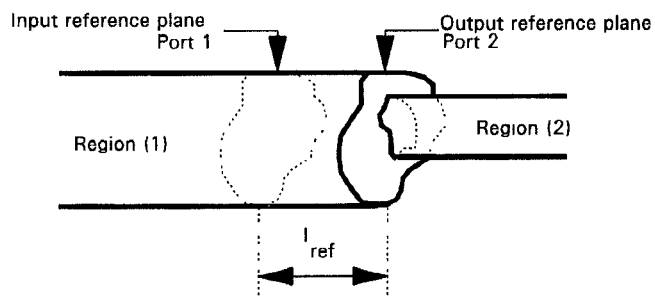


Fig. 1. Arbitrary planar waveguide junction studied in this paper.

In this paper a simple method is described for the evaluation of the multimode network representation of planar waveguide junctions in terms of admittance parameters or impedance parameters. The key feature of the method is that it starts from the wanted final results, the equivalent network representation, in order to obtain an analytic expression for the evaluation of the relevant matrix elements. The procedure is based on general network theory and is equivalent to ideally *measuring directly* the value of the admittance elements. The procedure is fully described leading to simple and elegant analytical expressions that are valid for arbitrary planar waveguide junctions. Measurement results of actual hardware are then compared with simulations indicating that the codes developed are indeed very accurate as well as computationally very efficient.

II. ADMITTANCE FORMULATION

The structure under investigation is the planar junction between two arbitrary waveguides shown in Fig. 1. The first step of the procedure is to define input and output reference planes (see Fig. 1), and to write formally the wanted final result in the form

$$I_m^{(\delta)} = \sum_{n=1}^{\infty} Y_{m,n}^{(\delta,1)} \cdot V_n^{(1)} + \sum_{n=1}^{\infty} Y_{m,n}^{(\delta,2)} \cdot V_n^{(2)}. \quad (1)$$

Where $I_m^{(\delta)}$ and $V_n^{(\delta)}$ are modal currents and voltages, respectively, and where δ can be 1 for region (1) or 2 for region (2), as shown in Fig. 1. The modal currents and voltages in (1) are the amplitude of the vector mode functions $\mathbf{h}_m^{(\delta)}$ and $\mathbf{e}_m^{(\delta)}$ relevant to the waveguide cross section (δ), as defined in [1], for instance. The above system of linear equations

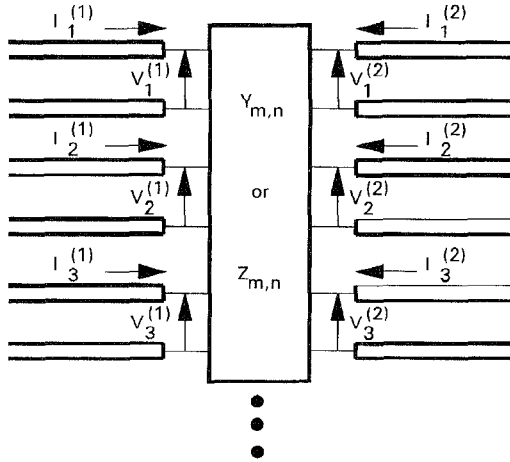


Fig. 2. Multimode equivalent network representation of the junction shown in Fig. 1, in terms of admittance parameters.

is equivalent to the network representation shown in Fig. 2 and can be easily solved with a computer program to study structures involving waveguide junctions. In order to analyze more complex waveguide structures, composed of a number of junctions connected by lengths of uniform waveguide, we only need to define a larger linear system which will have a simple block diagonal coefficient matrix. Each block will in fact represent either a junction or a length of connecting uniform waveguide.

The key point of the approach described in this paper is to note that (1) can also be used to obtain a formal expression for the evaluation of the $Y_{m,n}$ elements. We can, in fact, write directly

$$Y_{m,n}^{(\delta,\gamma)} = \frac{I_m^{(\delta)}}{V_n^{(\gamma)}}; \quad \forall V_i^{(\xi)} = 0, \quad i \neq n, \xi \neq \gamma. \quad (2)$$

Where γ and ξ can be again 1 for region (1) or 2 for region (2), respectively. This expression is the definition of $Y_{m,n}$ in terms of network theory and can also be used to *evaluate directly* the actual $Y_{m,n}$ elements, formally yielding

$$Y_{m,n}^{(\delta,\gamma)} = \frac{\int_{cs(\delta)} \mathbf{H}^{(\delta)}[V_n^{(\gamma)}] \cdot \mathbf{h}_m^{(\delta)} ds'}{V_n^{(\gamma)}} \quad (3)$$

where $\mathbf{H}^{(\delta)}[V_n^{(\gamma)}]$ is the transverse magnetic field generated at the port (δ) by the incident mode of amplitude $V_n^{(\gamma)}$ in port (γ).

To explain in detail the use of (2) and (3), let us begin with $Y_{m,n}^{(1,1)}$. For this elements, the definition in (2) requires a single mode incident from the left (port 1) (which corresponds to $V_n^{(1)} \neq 0$ and all other voltages in port 1 equal zero) and a short circuit in port 2 (corresponding to all voltages in region (2) equal zero). The resulting structure becomes the one shown in Fig. 3 and, as we can see, no discontinuities are actually present so that we can write directly

$$Y_{m,n}^{(1,1)} = (-j) \cdot Y_{0m}^{(1)} \cdot \cot(\beta_m^{(1)} l_{ref}) \cdot \delta_{m,n}. \quad (4)$$

Following the same philosophy for the elements $Y_{m,n}^{(2,1)}$, the definition in (2) requires again a single mode incident from

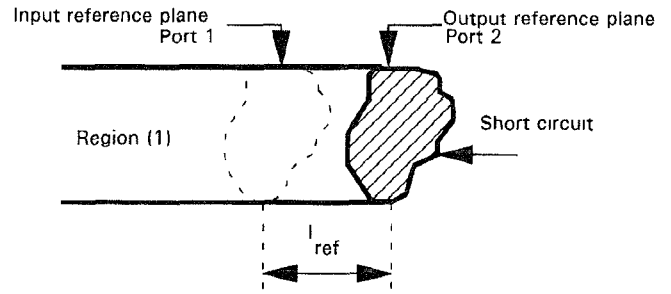


Fig. 3. Structure used for the evaluation of the $Y_{m,n}^{(1,1)}$ and $Y_{m,n}^{(2,1)}$. A short circuit is placed at port 2 of Fig. 1.

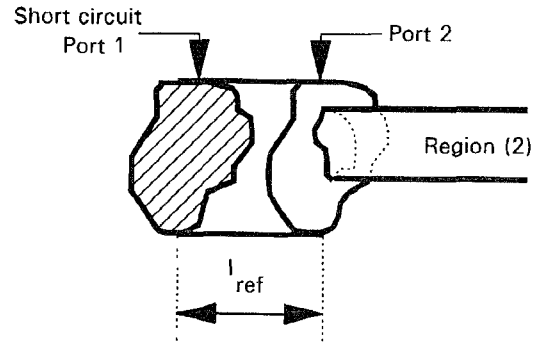


Fig. 4. Structure used for the evaluation of the $Y_{m,n}^{(2,2)}$. A short circuit is placed at port 1 of Fig. 1.

the left (port 1) and a short circuit in port 2. The current response will now be measured at port 2 using standard orthogonality, and the same structure in Fig. 3. Using then simple transmission line calculations, the resulting expression is

$$Y_{m,n}^{(2,1)} = (-j) \cdot \frac{Y_{0m}^{(1)}}{\sin(\beta_m^{(1)} l_{ref})} \cdot \langle \mathbf{h}_m^{(1)} \mathbf{h}_n^{(2)} \rangle \quad (5)$$

which can also be used for $Y_{n,m}^{(1,2)}$ since the junction is lossless and reciprocal.

Finally, for the elements $Y_{m,n}^{(2,2)}$, the definition in (2) requires a single mode exciting from the right (port 2) and a short circuit at port 1. The situation is therefore as indicated in Fig. 4, and the current response is again taken in port 2. To use (3), we now need to write an expression for $\mathbf{H}^{(2)}$. Taking into account the continuity of the transversal magnetic field at port (2), we can write

$$\mathbf{H}^{(2)} = \sum_{k=1}^{\infty} I_n^{(1)} \mathbf{h}_k^{(1)}. \quad (6)$$

Using again simple transmission line theory, we have

$$Y_{m,n}^{(2,2)} = \sum_{k=1}^{\infty} (-j) Y_{0k}^{(1)} \cdot \cot(\beta_k^{(1)} l_{ref}) \cdot \langle \mathbf{e}_k^{(1)} \mathbf{e}_n^{(2)} \rangle \cdot \langle \mathbf{h}_k^{(1)} \mathbf{h}_m^{(2)} \rangle. \quad (7)$$

This concludes the formal derivation of the multimode admittance matrix elements. What is important to note at this

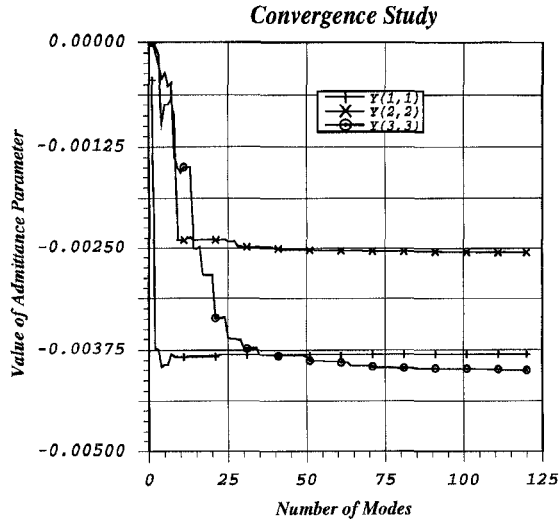


Fig. 5. Typical convergence behavior of the super (2,2) terms. The transition studied is from centered rectangular ($a = 19.05$ mm, $b = 9.525$ mm) to circular ($r = 13.6$ mm) waveguide.

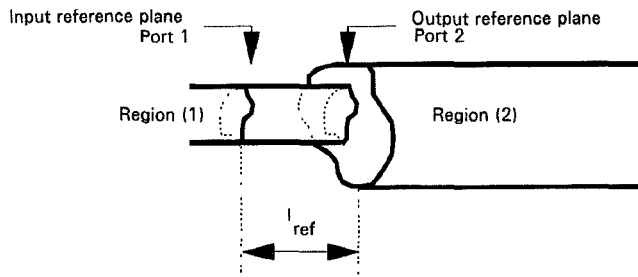


Fig. 6. Position of the reference planes in the case of an impedance representation.

point is the global structure of the admittance matrix that we have obtained. The matrix itself can be subdivided into four blocks. One block contains only single entries in the main diagonal (super (1,1) elements). Two more blocks contain identical single entry elements (super (1,2) and (2,1)). Only the last block (super (2,2) elements) requires the computation of summations. Furthermore, the running index of the summations affect only the modes of the larger waveguide, namely region (1). A typical example of convergence of the summations is shown in Fig. 5. The results shown in Fig. 5 are for a centered junction between a rectangular waveguide ($a = 19.05$ mm, $b = 9.525$ mm) and a circular waveguide ($r = 13.60$ mm), the length l_{ref} is 5 mm, the frequency of operation is 15 GHz. The parameters $Y_{1,1}$, $Y_{2,2}$, and $Y_{3,3}$ refer to the $TE_{1,0}$, $TE_{3,0}$, and $TE_{1,2}$ modes, respectively.

For the correct use of the network representation developed, the convergence of the admittance matrix elements must be verified in each case. The value of l_{ref} used can affect the convergence rate, however, a value of 5 mm has been found to be suitable for most applications.

In all of the equations derived the symbol $\langle \rangle$ stands for the scalar product of the quantities involved (coupling integrals resulting from the use of the orthogonality conditions) and e and h are the normalized vector mode functions of the

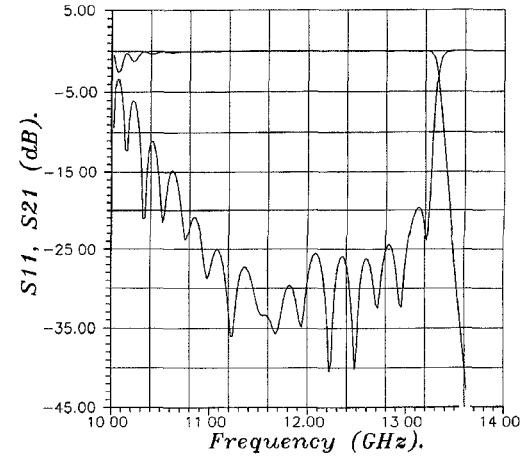


Fig. 7. Simulated results, obtained with the software developed, for the low-pass filter designed by COM DEV Canada. The filter is composed of 85 discontinuities.

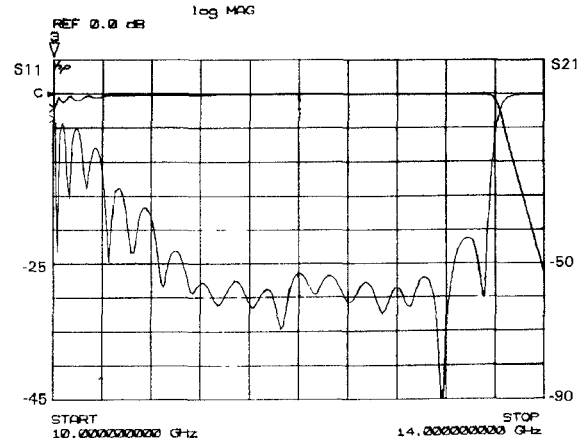
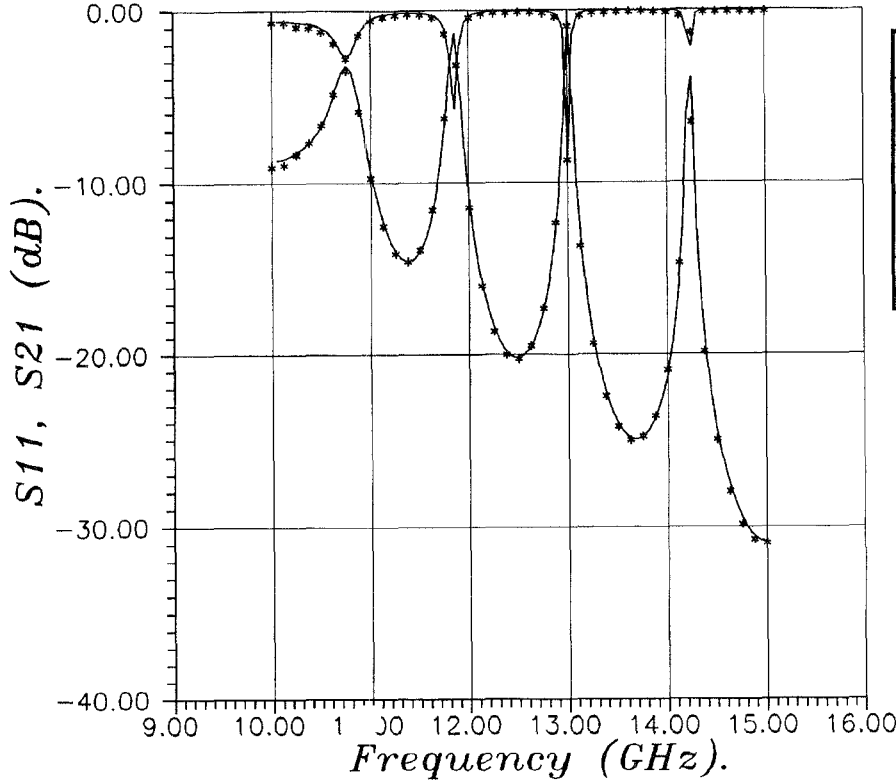


Fig. 8. Measured results of the low-pass filter designed by COM DEV Canada.

waveguides in the appropriate regions. $\beta_m^{(1)}$ is the propagation constant of the modes in region (1) and $Y_{0m}^{(1)}$ is the corresponding characteristic admittance. Moreover l_{ref} is the separation between the reference planes, as indicated in Fig. 1. The explicit analytic expressions of the scalar products are very simple, depend on the specific waveguide used, and are not reported here for the sake of space.

Admittance matrix descriptions for this type of problems have already been described in the past by several authors and have been identified with different names (see [4] and [5], for instance). A common feature of all these contributions, however, is that they are generally based on the theory of resonators, as described in [6]. As a result, the formulation always requires two type of modes (the so-called e and F modes) and all of the entries of the admittance matrix obtained contain summations over these modes. The method described in this paper is therefore substantially more efficient from a computational point of view. The only contribution (to the authors knowledge) can be found in the technical literature describing an approach similar to the one described here is [7], in the context of rectangular waveguide T-junctions.



Input Waveguide	Width	19.05 mm
	Height	9.52 mm
Input Window	Width	14.00 mm
Coupling	Height	1.00 mm
	Thickness	0.50 mm
Circular Resonator	Radius	11.70 mm
	Length	100.00 mm
Output Window	Width	18.00 mm
Coupling	Height	1.00 mm
	Thickness	0.50 mm
Output Waveguide	Width	19.05 mm
	Height	9.52 mm

Fig. 9. Comparison between simulated results and measurements for a single circular resonator with different input and output rectangular couplings. The stars are the measured results while the full line represents the results obtained with our approach

III. IMPEDANCE FORMULATION

The impedance formulation is essentially complementary to what has already been described for the admittance formulation. The starting point is the following mathematical description of the junction

$$V_m^{(\delta)} = \sum_{n=1}^{\infty} Z_{m,n}^{(\delta,1)} \cdot I_n^{(1)} + \sum_{n=1}^{\infty} Z_{m,n}^{(\delta,2)} \cdot I_n^{(2)}. \quad (8)$$

The location of the reference planes, however, must be changed to the one shown in Fig. 6, the multimode equivalent network representation obtained is the same as in Fig. 2, with the admittance matrix replaced by an impedance matrix. The formal definition of the generic $Z_{m,n}^{(\delta,\gamma)}$ element is now given by

$$Z_{m,n}^{(\delta,\gamma)} = \frac{V_m^{(\delta)}}{I_n^{(\gamma)}}; \quad \forall I_i^{(\xi)} = 0, \quad i \neq n, \xi \neq \gamma. \quad (9)$$

Furthermore, in the derivation of the explicit expressions of the $Z_{m,n}^{(\delta,\gamma)}$ elements, the short-circuit terminations used for the admittance case now become open-circuit terminations, as required by (9). The final expression thus obtained are

$$Z_{m,n}^{(1,1)} = (-j) \cdot Z_{0m}^{(1)} \cdot \cot(\beta_m^{(1)} l_{\text{ref}}) \cdot \delta_{m,n} \quad (10)$$

$$Z_{m,n}^{(2,1)} = (-j) \cdot \frac{Z_{0m}^{(1)}}{\sin(\beta_m^{(1)} l_{\text{ref}})} \cdot \langle e_n^{(1)} e_m^{(2)} \rangle \quad (11)$$

$$Z_{m,n}^{(2,2)} = \sum_{k=1}^{\infty} (-j) Z_{0k}^{(1)} \cdot \cot(\beta_k^{(1)} l_{\text{ref}}) \cdot \langle h_k^{(1)} h_n^{(2)} \rangle \cdot \langle e_k^{(1)} e_m^{(2)} \rangle. \quad (12)$$

IV. CHOOSING THE APPROPRIATE NETWORK REPRESENTATIONS

Two types of equivalent network representations have been developed, one based on an admittance multimode coupling matrix and the other based on an impedance matrix. The two representations are in principle absolutely equivalent. There is, however, a major difference between them that can be used to choose the more appropriate representation for a given problem. The difference comes from the analytical form of the matrix elements $Y_{m,n}^{(2,2)}$ and $Z_{m,n}^{(2,2)}$. To compute the values of these elements, a summation must be carried out, and the parameters involved are the order of the mode included in the final equivalent network and the index up to which the summation must be carried out. Normally, the number of modes included in the final network (m, n in (7) and (12)) is always less than 20, while the number of terms required in the summation (k in (7) and (12)) needs to be about 150 for good convergence. The summation index k , however, only involves the modes of the waveguide region in which the length l_{ref} is required and not the other.

If the modes of both waveguide regions of the junction have known analytical form, then the admittance or impedance formulation will give the same result with essentially the same computational effort. If one of the two waveguide regions has modes that are defined only numerically (as the result of an eigenvalue problem for an arbitrary waveguide, for instance) then choosing the representation that requires the length l_{ref} to be in the waveguide region that has the modes specified analytically will result in a much reduced computational effort.

V. EXAMPLES OF APPLICATIONS

As an example of application, the admittance formulation was used to develop a software tool for the analysis of rectangular to rectangular and circular to rectangular waveguide junctions. Since analytical expressions are available for the modes in all regions [1], the scalar products can be easily computed and the admittance elements are then obtained by direct application of the above equations. For the circular to rectangular junction, however, the required scalar products (coupling integrals) have been evaluated numerically. The theory proposed in [8] and [9] is used in this case to convert the surface integrals to line integrals over the contour of the rectangular waveguide, thus increasing the speed of the computation.

Once the admittance coupling matrices are computed, a complex structure, formed by cascading a number of discontinuities, can be easily studied by building a global multimode equivalent network representation. An important feature of this method is that the analysis of the global network thus obtained requires only one inversion per frequency point of a system of linear equations, which is banded and therefore can be performed very efficiently. No intermediate inversions are required for the computation of the *s*-parameters of the complete structure, thus leading to codes that can be very fast even for very complex structures.

The software developed was first used to analyze a low-pass filter composed of 85 discontinuities (capacitive steps) including two double-plane step transformers at the input and the output of the filter. The filter has been designed by COM DEV Canada and manufactured by COM DEV UK. The simulated results obtained with our software are shown in Fig. 7 and can be compared with the measured response of the hardware in Fig. 8. It can be seen that the agreement is very good. The only factor of disagreement is in a frequency shift of about 200 MHz due to the fact that the hardware was manufactured with rounded corners while in the simulation all corners are assumed to be without any radius.

For this analysis 20 modes were used in the final equivalent network and 150 terms were used for the summation of the admittance elements. The analysis was performed on an IBM RISC 6000 platform and the time spent by the software for the analysis of the complete structure was 1 min per each frequency point.

As a further example, the analysis of a circular waveguide resonator with different input and output rectangular-window couplings was performed. In Fig. 9 we present the comparison between simulated results and measurements (the stars) showing a very good agreement. For this analysis four modes were used in all uniform waveguide sections and 120 modes were used to sum the admittance elements. The time spent by the software for the analysis of the complete structure was 2 min and 10 sec for 100 frequency points.

VI. CONCLUSION

A simple and elegant procedure has been developed for the evaluation of the multimode equivalent network representation of a planar junction between arbitrary waveguides in terms

of an admittance or an impedance matrix. The expressions derived can be used for arbitrary waveguides and are particularly simple to use if the modes of at least one of the two waveguides involved are known analytically.

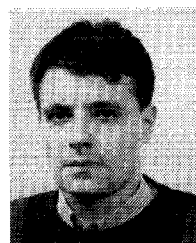
Comparison between the results of simulations obtained with software based on the procedure described and measurements of actual hardware have also been presented indicating that the new procedure indeed leads to very accurate and fast CAD tools.

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